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DATE MAILED: 12/16/2004

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/639,580	08/13/2003	Masanao Horie	8008-1045	7685	
466	7590 12/16/2004		EXAMINER		
YOUNG & THOMPSON			EVERHART, CARIDAD		
2ND FLOOR	23RD STREET		ART UNIT	PAPER NUMBER	
ARLINGTON, VA 22202			2825		

Please find below and/or attached an Office communication concerning this application or proceeding.

<u>.</u> -		Application No.	Applicant(s)				
Office Action Summary		10/639,580	HORIE, MASANAO				
		Examiner	Art Unit				
		Caridad M. Everhart	2825	A.			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status	•						
1)[Responsive to communication(s) filed on	·					
2a) <u></u>	This action is FINAL . 2b)⊠ Th	nis action is non-final.					
3)	3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
	closed in accordance with the practice unde	r <i>Ex parte Quayle</i> , 1935 C.D. 11, 45	53 O.G. 213.				
Dispositi	on of Claims			•			
4)⊠	Claim(s) 1-8 is/are pending in the application	1					
4a) Of the above claim(s) is/are withdrawn from consideration.							
5) Claim(s) is/are allowed.							
6)⊠	6)⊠ Claim(s) <u>1-8</u> is/are rejected.						
	Claim(s) is/are objected to.						
8)	Claim(s) are subject to restriction and	or election requirement.					
Applicati	on Papers						
9)	The specification is objected to by the Exami	ner.					
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority u	ınder 35 U.S.C. § 119						
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)⊠ All b)□ Some * c)□ None of:							
1. Certified copies of the priority documents have been received.							
2. Certified copies of the priority documents have been received in Application No							
3. Copies of the certified copies of the priority documents have been received in this National Stage							
application from the International Bureau (PCT Rule 17.2(a)).							
* See the attached detailed Office action for a list of the certified copies not received.							
Attachmen	t(s)						
	e of References Cited (PTO-892)	4) Interview Summary					
	e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/0	Paper No(s)/Mail Da 8) 5) Notice of Informal P		D-152)			
	r No(s)/Mail Date <u>8-13-2003</u> .	6) Other:	144	1			

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Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

The factual inquiries set forth in *Graham* v. *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

- 1. Determining the scope and contents of the prior art.
- 2. Ascertaining the differences between the prior art and the claims at issue.
- 3. Resolving the level of ordinary skill in the pertinent art.
- 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

Claims 1-8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Irie(US 6,534,386B2) in view of Sakamoto, et al. (UA 2002/0028525A1).

Irie discloses the steps of dicing a wafer to form chips(col. 2, lines 10-11), providing gaps between the chips(col. 2, lines 14-15), filling the gaps with resin(col. 2 lines 13-16). There is also a sheet to which the die are adhered(col. 3, lines 23-26). The sealing resin is applied and the chips are separated(col. 3, lines 39-41 and 60-65). Connection terminals are provided and external connection terminals to which the connections terminals of the chips can be connected and which are continuous with external wiring(col. 7, lines 14-20) are provided with bumps formed as external connection terminals. The sheet is drawn, which is the same as stretched(col. 3, lines 58-65) and

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the chips are separated (col. 4,lines 24-33). Although the word isotropically is not used, Fig. 3 indicates that the stretching is isotropic by indicating the directions with arrows. Irie is silent with respect to testing and with respect to alignment marks.

Sakamoto et al disclose a method in which chips are adhered to an adhesive sheet, and alignment markings are used to align the chips for singulation and in which the chips are tested while mounted on the sheet(paragraphs 0063, 0047, and 0089).

It would have been obvious to one of ordinary skill in the art at the time of the invention to have used alignment marks in the singulation step and to have tested the chips while mounted on the sheet in the process taught by Irie because it is conventional in the art to use alignment marks in a step such as singulation and because the testing of the chips while on the sheet would have made the testing of a large number of chips easier. In addition, Irie implies that this can be done in the disclosure cited above that the terminals of the chips can be connected and that these connections can be made continuous with external wiring, which also makes the testing of a large number of chips easier.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Caridad M. Everhart whose telephone number is 571-272-1892. The examiner can normally be reached on Monday through Fridays 7:30-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on 571-272-1907. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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PRIMARY EXAMINE

C. Everhart 12-10-2004